

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Chun-Gyoo LEE, *et al.*

Application No.: To be assigned

Filed: Herewith

For: **METHOD FOR DRIVING A FLAT
PANEL DISPLAY (AS AMENDED)**

Art Unit: To be assigned

Examiner: To be assigned

Atty. Docket: 6161.0010.C2

Preliminary Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA. 22313-1450

Sir:

Please amend the above-identified patent application as follows.

Amendments

In the Specification:

At the beginning of the specification, please enter the following paragraph.

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CROSS REFERENCE

This is a continuation application of the co-pending U.S. Patent Application Serial No. 09/967,936 filed on October 2, 2001, which has now become U.S. Patent No. 6,617,798. –

In the Claims:

Please cancel claims 1-22 without any disclaimer and a prejudice to and add the following claims.

Listing of claims is as follows:

1-22. (Cancelled)

23. (Currently Added) A method for driving a flat panel display, the flat panel display including a back plate, a gate electrode, a cathode electrode, a planar field emission source formed on the cathode electrode, a grid plate with a first grid electrode and a second grid electrode and having a plurality of apertures corresponding to a pixel area, and a faceplate spaced from the grid plate and having a screen formed on one surface thereof facing the grid plate, the method comprising the steps of:

applying scan voltage to the cathode electrode and data signal voltage to the first grid electrode to form an electric field by emitting electrons from the planar field emission source;

applying field enhancing signal voltage to the gate electrode to enhance the electric field around the planar field emission source;

applying focus signal voltage to the second grid electrode to converge the electrons at the apertures of the grid plate; and

applying anode signal voltage to the screen to accelerate the electrons toward the screens.

24. (Currently Added) The method of claim 23, wherein the gate electrode is formed of a single layer.

25. (Currently Added) The method of claim 23, wherein the second grid electrode is formed of a single layer.

26. (Currently Added) A method for driving a flat panel display, the flat panel display including a back plate, a gate electrode, a cathode electrode, a planar field emission source formed on the cathode electrode, a mesh plate having a plurality of apertures corresponding to a pixel area, and a faceplate spaced from the grid plate and having a screen formed on one surface thereof facing the mesh plate, the method comprising the steps of:

applying scan voltage to the cathode electrode and data signal voltage to the gate electrode;

applying focus signal voltage to the mesh plate to converge the electrons at the apertures of the mesh plate; and

applying anode signal voltage to the screen to accelerate the electrons toward the screen.